

### REMARKS/ARGUMENTS

Claims 1-4 are pending in the application. Reexamination and reconsideration are hereby requested.


Claims 1-3 were rejected as unpatentable over Brooks in view of Frankel. The Examiner added Frankel to Brooks as showing a framework on the DSP.

Applicants reply that Brooks has direct control of DSP resources by the host CPU, see col.6, ln.65 et seq.; so there would be no suggestion of combination with the RTOS for the DSP of Frankel.

Claim 4 was rejected as unpatentable over Frankel in view of Kramer. The Examiner pointed to Fig.2.

Applicants reply that step (h) of claim 4 requires designation of a third buffer, and is beyond suggestion by the buffer swapping of Frankel.

Respectfully submitted,

  
Carlton H. Hoel  
Reg. No. 29,934  
Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, Texas 75265  
972.917.4365